

ΠΑΡΑΡΤΗΜΑ Ι: ΛΙΣΤΑ ΔΗΜΟΣΙΕΥΣΕΩΝ

ΑΡΘΡΑ ΣΕ ΠΕΡΙΟΔΙΚΑ

-
- Π.1. **K. Yiannopoulos**, S. Thomas, and J. Azaña, "Amplitude/Timing Jitter and Peak-to-Peak Variation Optimal Rate Multipliers," *IEEE/OSA Journal of Lightwave Technology*, *IEEE/OSA Journal of Lightwave Technology*, vol. 26, no. 8, pp. 899-907, April 2008.
(<http://dx.doi.org/10.1109/JLT.2008.917371>)
- Π.2. **K. Yiannopoulos**, E. Varvarigos, and K. Vlachos, "Multiple-Input Buffer and Shared Buffer Architectures for Optical Packet and Burst Switching Networks," *IEEE/OSA Journal of Lightwave Technology*, vol. 25, no. 6, pp. 1379-1389, June 2007.
(<http://dx.doi.org/10.1109/JLT.2007.896804>)
- Π.3. **K. Yiannopoulos**, G.T. Kanellos, N. Pleros, and H. Avramopoulos, "Additive Noise and Jitter Performance Analysis of Passive Optical Interferometers Operated at Ultra High Rates," *IEEE Journal of Quantum Electronics*, vol. 42, no. 9, pp. 918-925, September 2006.
(<http://dx.doi.org/10.1109/JQE.2006.880070>)
- Π.4. **K. Yiannopoulos**, K. Vyrsokinos, D. Tsiokos, E. Kehayas, N. Pleros, G. Theophilopoulos, T. Houbavlis, G. Guekos, and H. Avramopoulos, "Pulse Repetition Frequency Multiplication with Spectral Selection in Fabry-Pérot Filters," *IEEE Journal of Quantum Electronics*, vol. 40, no. 2, pp. 157-165, February 2004.
(<http://dx.doi.org/10.1109/JQE.2003.821534>)
- Π.5. **K. Yiannopoulos**, L. Stampoulidis, T. Houbavlis, and H. Avramopoulos, "Generation of 40-GHz control signals from flag pulses for switching all-optical gates for use with optical packets," *OSA Optics Letters*, vol. 29, no. 3, pp. 241-243, February 1 2004.
(<http://dx.doi.org/10.1364/OL.29.000241>)
- Π.6. **K. Yiannopoulos**, K. Vyrsokinos, E. Kehayas, N. Pleros, K. Vlachos, H. Avramopoulos, and G. Guekos, "Rate Multiplication by Double-Passing Fabry-Pérot Filtering," *IEEE Photonics Technology Letters*, vol. 15, no. 9, pp. 1294-1296, September 2003.
(<http://dx.doi.org/10.1109/LPT.2003.816691>)
- Π.7. **K. Yiannopoulos**, K. Vyrsokinos, N. Pleros, D. Tsiokos, C. Bintjas, G. Guekos, and H. Avramopoulos, "Repetition Rate Upgrade for Optical Sources," *IEEE Photonics Technology Letters*, vol. 15, no. 6, pp. 861-863, June 2003.
(<http://dx.doi.org/10.1109/LPT.2003.811343>)
- Π.8. O. Zouraraki, **K. Yiannopoulos**, P. Zakynthinos, D. Petrantonakis, E. Varvarigos, A. Poustie, G. Maxwell, and H. Avramopoulos, "Implementation of an All-Optical Time-Slot-Interchanger Architecture," *IEEE Photonics Technology Letters*, vol. 19, no. 17, pp. 1307-1309, September 1 2007.
(<http://dx.doi.org/10.1109/LPT.2007.902331>)

- Π.9. M. Kalyvas, K. Yiannopoulos, T. Houbavlis, and H. Avramopoulos, "Control Signal Generation from Flag Pulses to Drive All-Optical Gates," *IEEE Photonics Technology Letters*, vol. 15, no. 9, pp. 1122-1124, April 2004.
(<http://dx.doi.org/10.1109/LPT.2004.824611>)
- Π.10. L. Stampoulidis, K. Yiannopoulos, T. Houbavlis, and H. Avramopoulos, "Generation of 40 GHz all-optical gate control signals from flag pulses," *SPIE Optical Engineering*, vol. 43, no. 1, pp. 14-15, January 2004.
(<http://dx.doi.org/10.1117/1.1633274>)
- Π.11. M. Kalyvas, K. Yiannopoulos, G. Theophilopoulos, T. Houbavlis, H. Avramopoulos, and G. Guekos, "All-optical packet clock generation from decision pulses," *SPIE Optical Engineering*, vol. 42, no. 12, pp. 3413-3414, December 2003.
(<http://dx.doi.org/10.1117/1.1624612>)
- Π.12. K. Vyrsoinos, K. Yiannopoulos, E. Kehayas, N. Pleros, T. Houbavlis, and H. Avramopoulos, "Repetition frequency quadruplication through Fabry-Pérot filtering," *SPIE Optical Engineering*, vol. 42, no. 11, pp. 3075-3076, November 2003.
(<http://dx.doi.org/10.1117/1.1618232>)
- Π.13. M. Kalyvas, K. Yiannopoulos, T. Houbavlis, and H. Avramopoulos, "Design Algorithm of All-Optical Linear Feedback Shift Registers," *AEÜ - International Journal of Electronics and Communications*, vol. 57, no. 5, pp. 328-332, September 2003.
(<http://dx.doi.org/10.1078/1434-8411-54100179>)
- Π.14. C. Bintjas, K. Yiannopoulos, N. Pleros, G. Theophilopoulos, M. Kalyvas, H. Avramopoulos, and G. Guekos, "Clock Recovery Circuit for Optical Packets," *IEEE Photonics Technology Letters*, vol. 14, no. 9, pp. 1363-1365, September 2002.
(<http://dx.doi.org/10.1109/LPT.2002.801095>)
- Π.15. G. Theophilopoulos, M. Kalyvas, K. Yiannopoulos, K. Vlachos, E. Varvarigos, and H. Avramopoulos, "An alternative implementation perspective for the 'Scheduling Switch' architecture," *IEEE/OSA Journal of Lightwave Technology*, vol. 23, no. 2, pp. 732-739, February 2005.
(<http://dx.doi.org/10.1109/JLT.2004.838857>)
- Π.16. C. Bintjas, N. Pleros, K. Yiannopoulos, G. Theophilopoulos, M. Kalyvas, H. Avramopoulos, and G. Guekos, "All-Optical Packet Address and Payload Separation," *IEEE Photonics Technology Letters*, vol. 14, no. 12, pp. 1728-1730, December 2002.
(<http://dx.doi.org/10.1109/LPT.2002.804654>)
- Π.17. T. Houbavlis, K.E. Zoiros, M. Kalyvas, G. Theophilopoulos, C. Bintjas, K. Yiannopoulos, N. Pleros, K. Vlachos, H. Avramopoulos, L. Schares, L. Occhi, G. Guekos, J.R. Taylor, S. Hansmann, and W. Miller, "All-optical signal processing and applications within the ESPRIT project DO_ALL," (*Invited Paper*) *IEEE/OSA Journal of Lightwave Technology*, vol. 23, no. 2, pp. 781-801, February 2005.
(<http://dx.doi.org/10.1109/JLT.2004.838854>)
- Π.18. N. Pleros, K. Vyrsoinos, C. Bintjas, K. Yiannopoulos, K. Vlachos, H. Avramopoulos, and G. Guekos, "All-Optical Clock Recovery From Short Asynchronous Data Packets at

10 Gb/s," *IEEE Photonics Technology Letters*, vol. 15, no. 9, pp. 1291-1293, September 2003.

(<http://dx.doi.org/10.1109/LPT.2003.816117>)

Π.19. G. Theophilopoulos, M. Kalyvas, C. Bintjas, N. Pleros, K. Yiannopoulos, A. Stavdas, H. Avramopoulos, and G. Guekos, "Optically Addressable 2x2 Exchange/Bypass Packet Switch," *IEEE Photonics Technology Letters*, vol. 14, no. 7, pp. 998-1000, July 2002.

(<http://dx.doi.org/10.1109/LPT.2002.1012411>)

Π.20. N. Pleros, C. Bintjas, M. Kalyvas, G. Theophilopoulos, K. Yiannopoulos, S. Sygletos, and H. Avramopoulos, "Multiwavelength and Power Equalized SOA Laser Sources," *IEEE Photonics Technology Letters*, vol. 14, no. 5, pp. 693-695, May 2002.

(<http://dx.doi.org/10.1109/68.998728>)

- Σ.1. S. Thomas, J. Azaña, and K. Yiannopoulos, "Optimal Timing Jitter Rate Multipliers Based on Sampled Fiber Bragg Gratings," in *Proc. LEOS 2007 Annual Meeting*, pp. 926-927.
(<http://dx.doi.org/10.1109/LEOS.2007.4382713>)
- Σ.2. K. Yiannopoulos, E. Varvarigos, and K. Vlachos, "Multiple-Input Buffer and Shared Buffer Architectures for Asynchronous Optical Burst Switching Networks," in *Proc. IEEE International Conference on Communications (ICC) 2007*, pp. 2283 – 2288.
(<http://dx.doi.org/10.1109/ICC.2007.383>)
- Σ.3. K. Yiannopoulos, K. Vlachos and E. Varvarigos, "An Architecture for Almost All-Optical Nodes Based on Wavelength Converters," in *Proc. Communication Systems, Networks and Digital Signal Processing (CSNDSP) 2006*, pp. 391-395.
(<http://www.wcl.ee.upatras.gr/csndsp/CD/contents/Sessions/Presentations/B3%20-%20Optical%20Systems%20and%20Networks%20II/B3.2.PDF>)
- Σ.4. K. Yiannopoulos, K. Vyrsokinos, N. Pleros, C. Bintjas, T. Houbavlis, H. Avramopoulos, and G. Guekos, "A Repetition Rate Upgrade from 10 to 40 GHz for Laser Sources," in *Proc. Optical Fiber Communication Conference (OFC) 2003*, vol. 2, pp. 765-766.
(<http://ieeexplore.ieee.org/search/wrapper.jsp?arnumber=1248555>)
- Σ.5. K. Yiannopoulos, N. Pleros, C. Bintjas, M. Kalyvas, G. Theophilopoulos, H. Avramopoulos, and G. Guekos, "All-Optical Packet Clock Recovery Circuit," in *Proc. European Conference on Optical Communication (ECOC) 2002*, vol. 2, paper 4.3.5.
(<http://ieeexplore.ieee.org/search/wrapper.jsp?arnumber=1601005>)
- Σ.6. K. Yiannopoulos, N. Pleros, C. Bintjas, M. Kalyvas, G. Theophilopoulos, H. Avramopoulos, and G. Guekos, "All Optical Clock Recovery for Data Packets," in *Proc. IEEE/LEOS Summer Topical Meeting 2002 (All-Optical Networking: Existing and Emerging Architecture and Applications / Dynamic Enablers of Next-Generation Optical Communications Systems / Fast Optical Processing in Optical Transmission/VCSEL and Microcavity Lasers)*, pp. 35-36.
(<http://dx.doi.org/10.1109/LEOSST.2002.1027587>)
- Σ.7. M. Spyropoulou, K. Yiannopoulos, S. Sygletos, K. Vlachos, and I. Tomkos, "160 Gbps Simulation of a Quantum Dot Semiconductor Optical Amplifier Based Optical Buffer," in *Proc. 11th Conference on Optical Network Design and Modelling (ONDM) 2007*, vol. 4534, pp. 107-116, May 2007.
(http://dx.doi.org/10.1007/978-3-540-72731-6_13)
- Σ.8. L. Stampoulidis, K. Yiannopoulos, M. Kalyvas, G. Theophilopoulos, and H. Avramopoulos, "All-optical control signal generation to drive optical gates for packet switching applications," in *Proc. Optical Amplifiers and Their Applications (OSA/OAA) 2004*, pp. JWB15.
(<http://www.opticsinfobase.org/abstract.cfm?URI=OAA-2004-JWB15>)
- Σ.9. G. Theophilopoulos, K. Yiannopoulos, M. Kalyvas, C. Bintjas, G. Kalogerakis, H. Avramopoulos, L. Occhi, L. Schares, G. Guekos, S. Hansmann, and R. Dall' Ara "40

GHz All-Optical XOR with UNI Gate," in *Proc. Optical Fiber Communication Conference (OFC) 2001*, vol. 1, pp. MB2/1-MB2/3.

<http://dx.doi.org/10.1109/OFC.2001.927824>

- Σ.10. O. Zouraraki, D. Petrantonakis, K. Yiannopoulos, R. Meleiro, L. M. Sadeghioon, A. Poustie, G. Maxwell, E. Varvarigos, K. Vlachos, P. Monteiro, D. Simeonidou and H. Avramopoulos, "Optically-Addressable Packet Timeslot Interchanger Using a Quadruple Switch Array," in *Proc. Optical Fiber Communication Conference (OFC) 2007*, pp. OTuB3.

<http://dx.doi.org/10.1109/OFC.2007.4348766>

- Σ.11. K. Christodoulopoulos, K. Vlachos, K. Yiannopoulos, and E.A. Varvarigos, "Relaxing Delayed Reservations: An approach for Quality of Service differentiation in Optical Burst Switching networks," in *Proc. International Conference on Broadband Communications, Networks, and Systems (BROADNETS) 2006*.

<http://dx.doi.org/10.1109/BROADNETS.2006.4374364>

- Σ.12. N. Pleros, C. Bintjas, K. Yiannopoulos, G. Theophilopoulos, M. Kalyvas, K. Vyrsokinos, H. Avramopoulos, and G. Guekos, "All-Optical Address and Data Separation for 10 Gb/s Packets," in *Proc. European Conference on Optical Communication (ECOC) 2002*, vol. 3, paper P4.10.

<http://ieeexplore.ieee.org/search/wrapper.jsp?arnumber=1601264>

1. "Enabling Optical Internet with Advanced Network Technologies," Javier Aracil and Franco Callegati (Eds.), Springer-Verlag 2009, ISBN: 978-1-84882-277-1.

<http://dx.doi.org/10.1007/978-1-84882-278-8>

K. Vlachos, L. Raptis, A. Teixeira, G.M. Tosi Beleffi, and K. Yiannopoulos, "Chapter 3: Optical Switch Fabrics and their Application, Paragraph: 3.3. Semiconductor Optical Amplifier Switches," pp. 155-190.

http://dx.doi.org/10.1007/978-1-84882-278-8_6

2. "Towards Digital Optical Networks," I. Tomkos, M. Spyropoulou, K. Ennser, M. Köhn, B. Mikac (Eds.), Lecture Notes in Computer 5412, Springer-Verlag 2009, ISBN: 978-3-642-01523-6.

<http://dx.doi.org/10.1007/978-3-642-01524-3>

W. Kabacinski, J. Chen, G. Danilewicz, J. Kleban, M. Spyropoulou, I. Tomkos, E. Varvarigos, K. Vlachos, S. Weclowski, L. Wosinska, and K. Yiannopoulos, "Chapter 5: Novel Switch Architectures," pp. 133-160.

http://dx.doi.org/10.1007/978-3-642-01524-3_6
